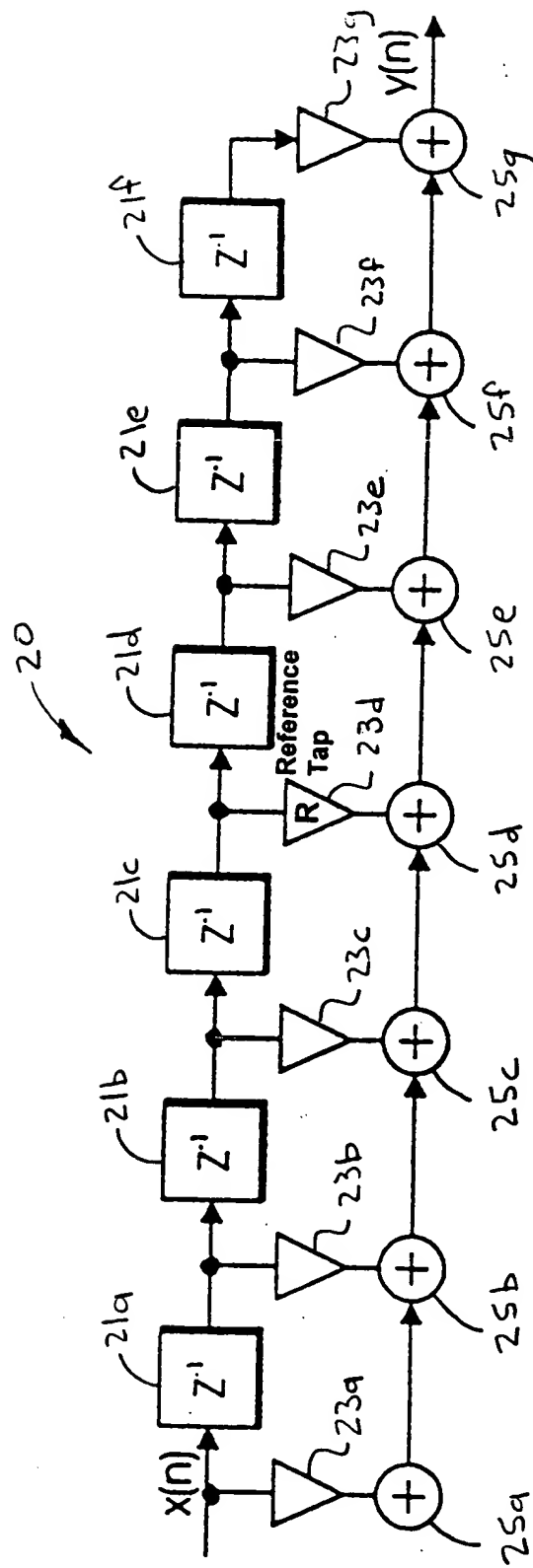
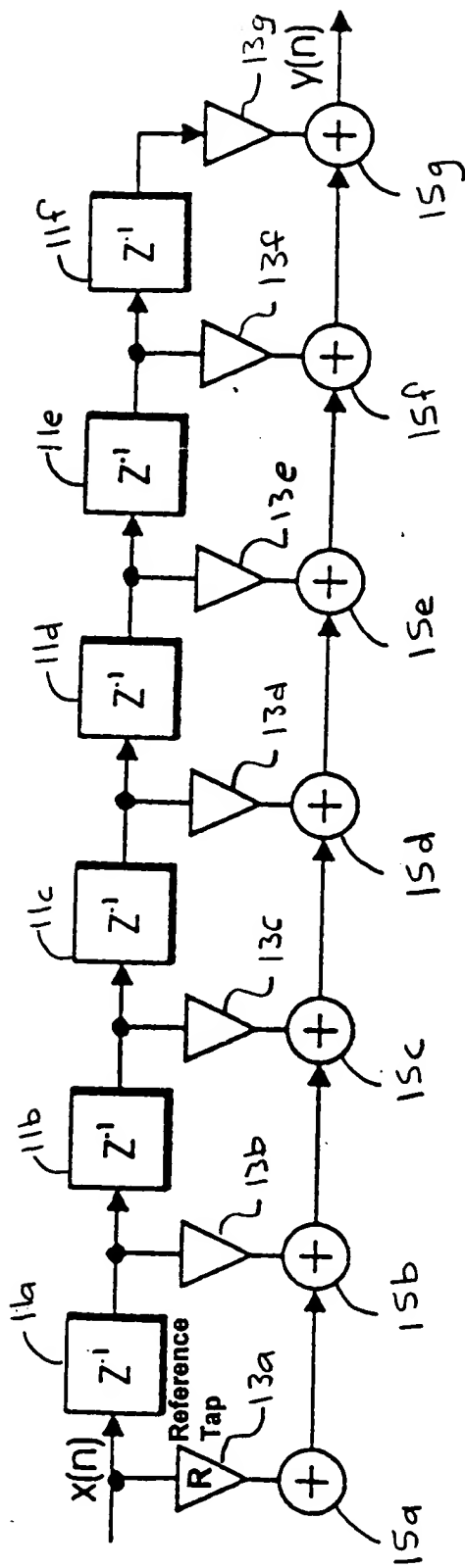
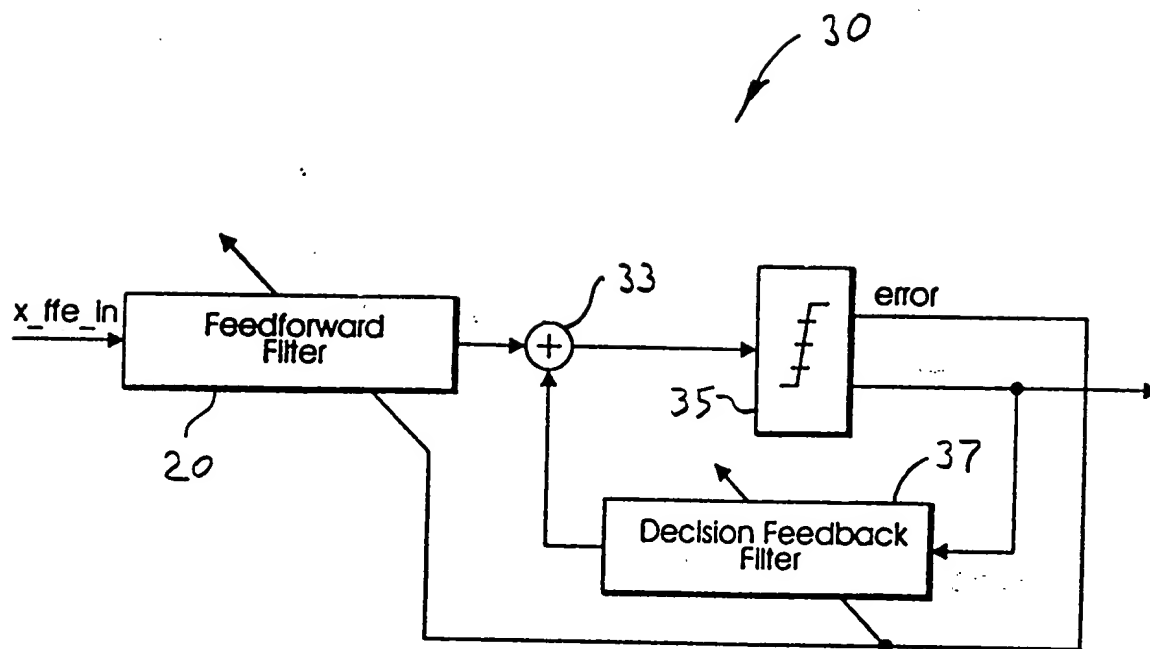


**FIG. 1**  
*Prior Art*



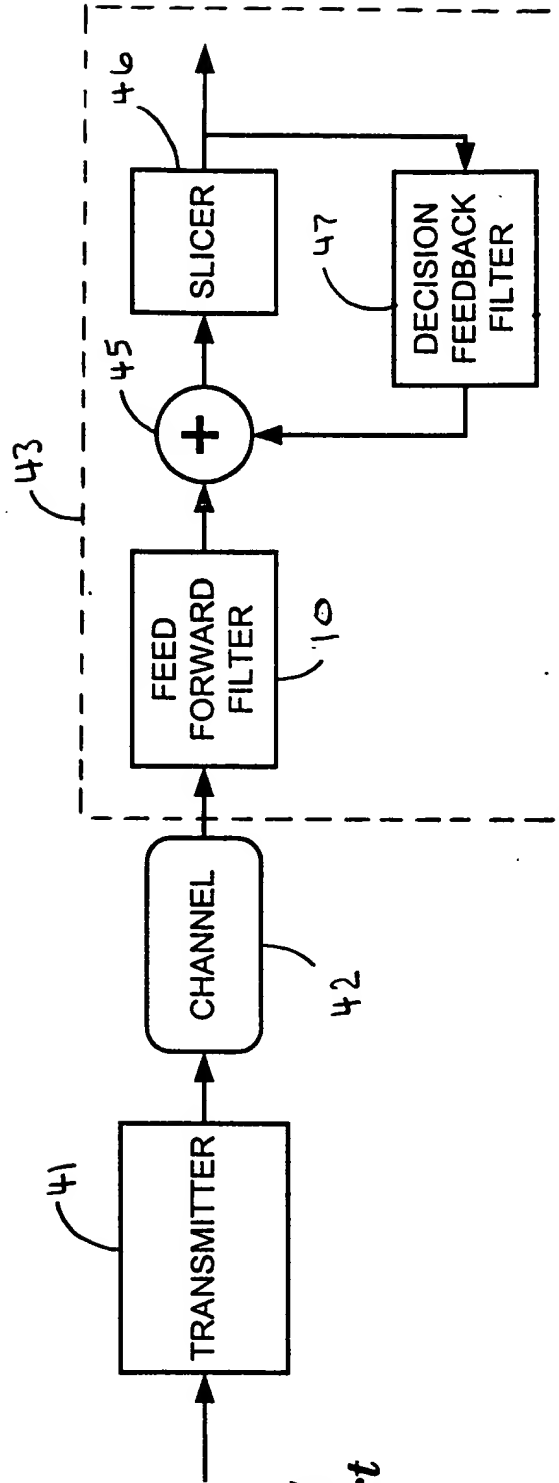
**FIG. 2**



**FIG. 3**

FIG. 4  
Prior Art

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FIG. 5

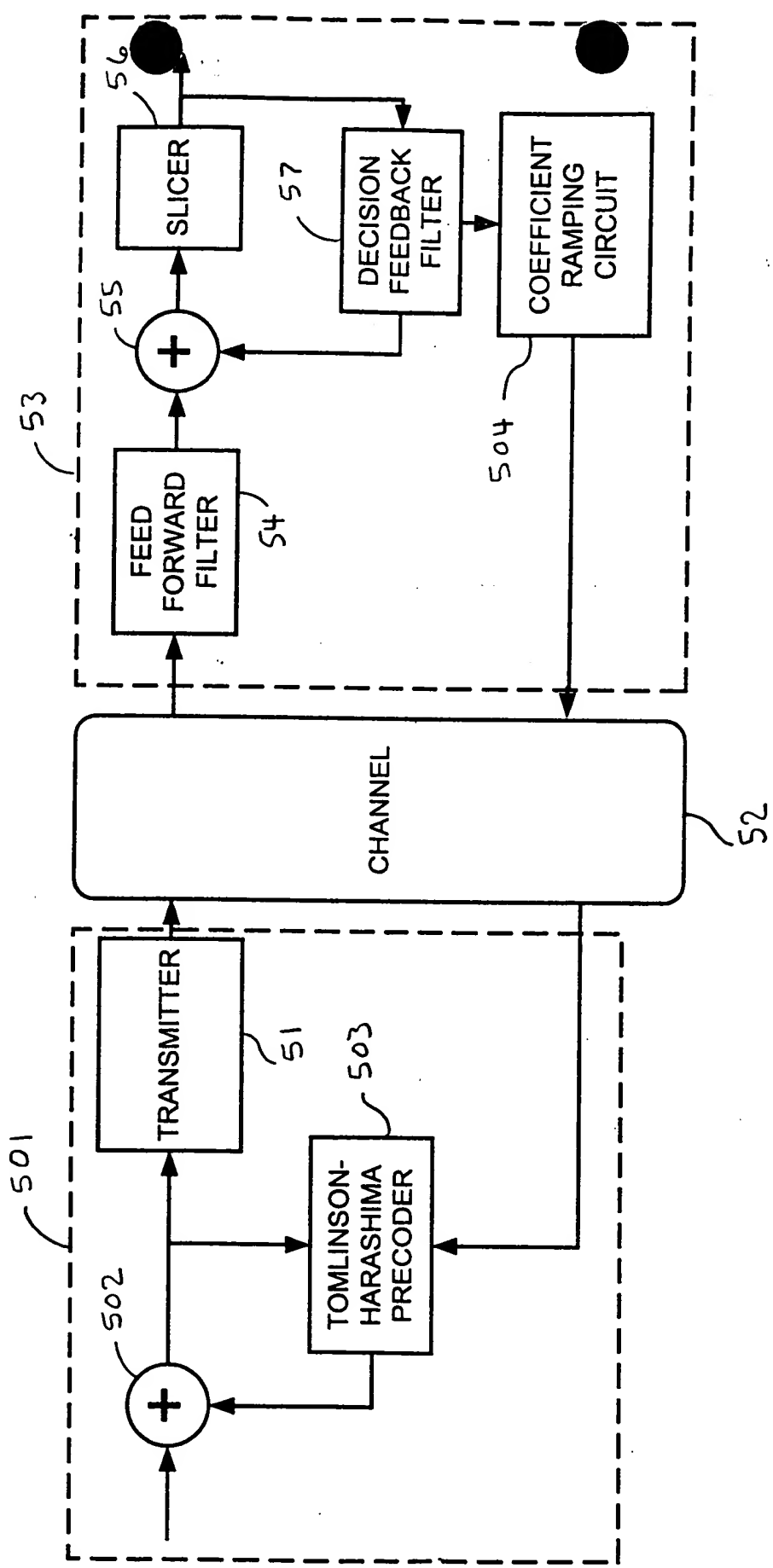
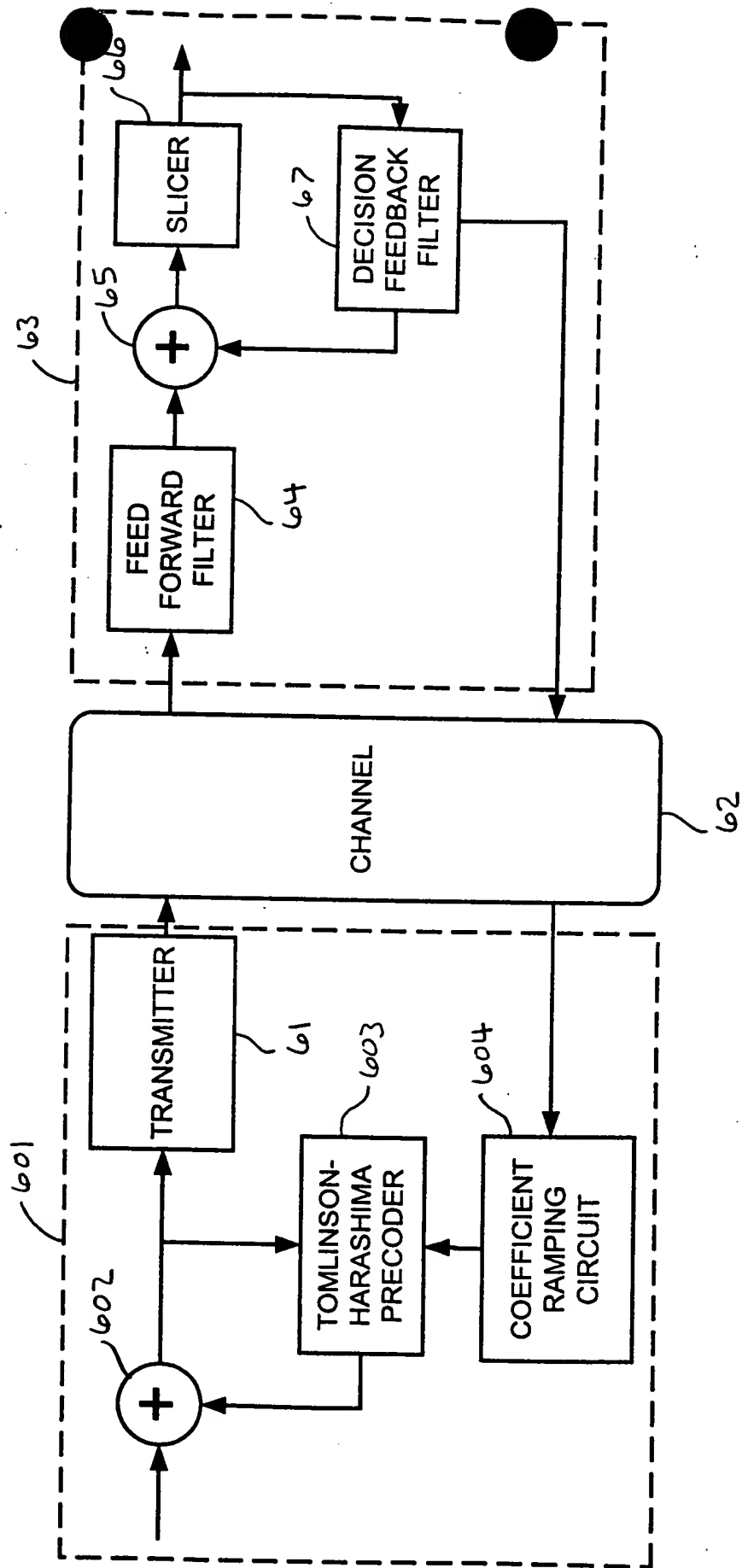
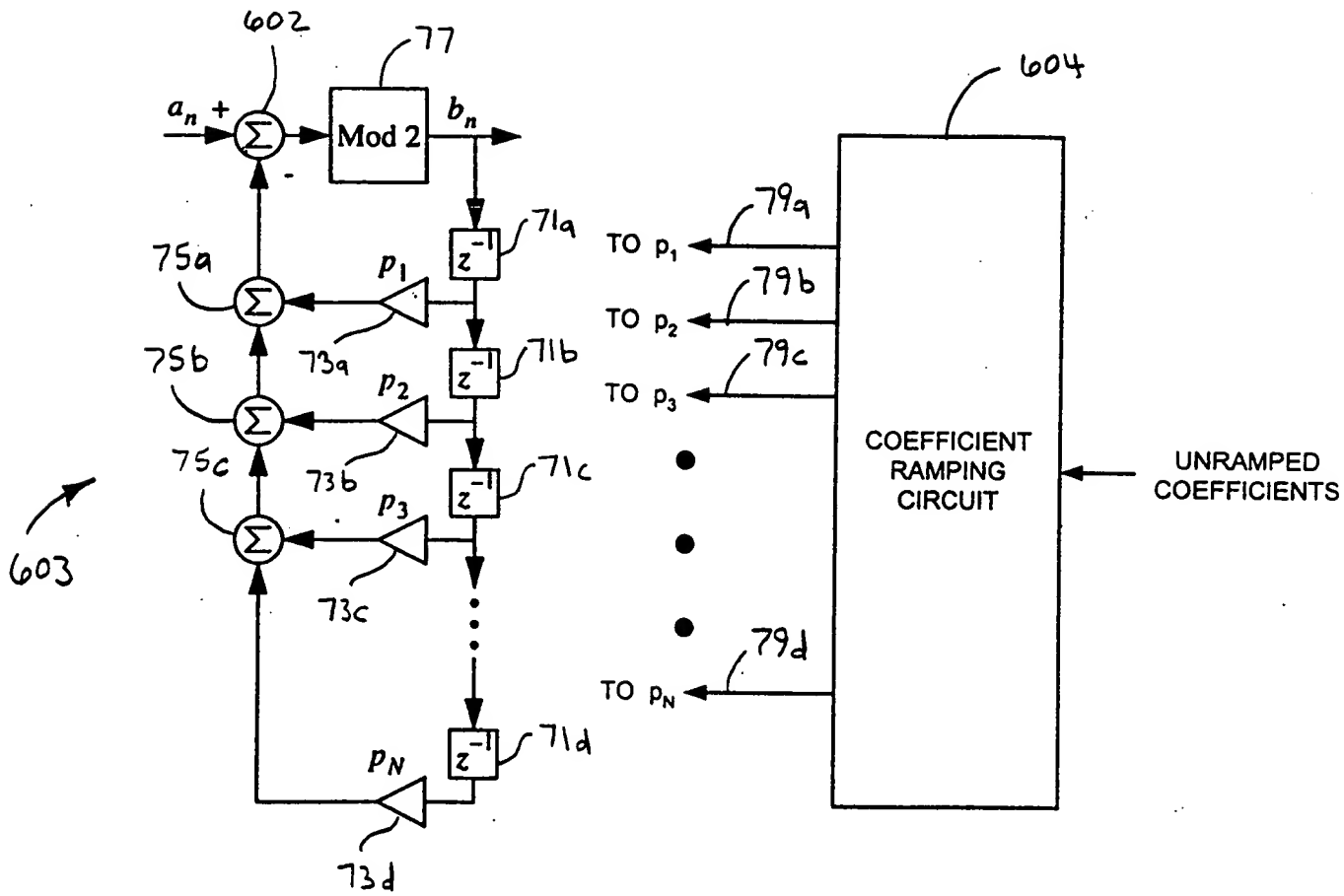


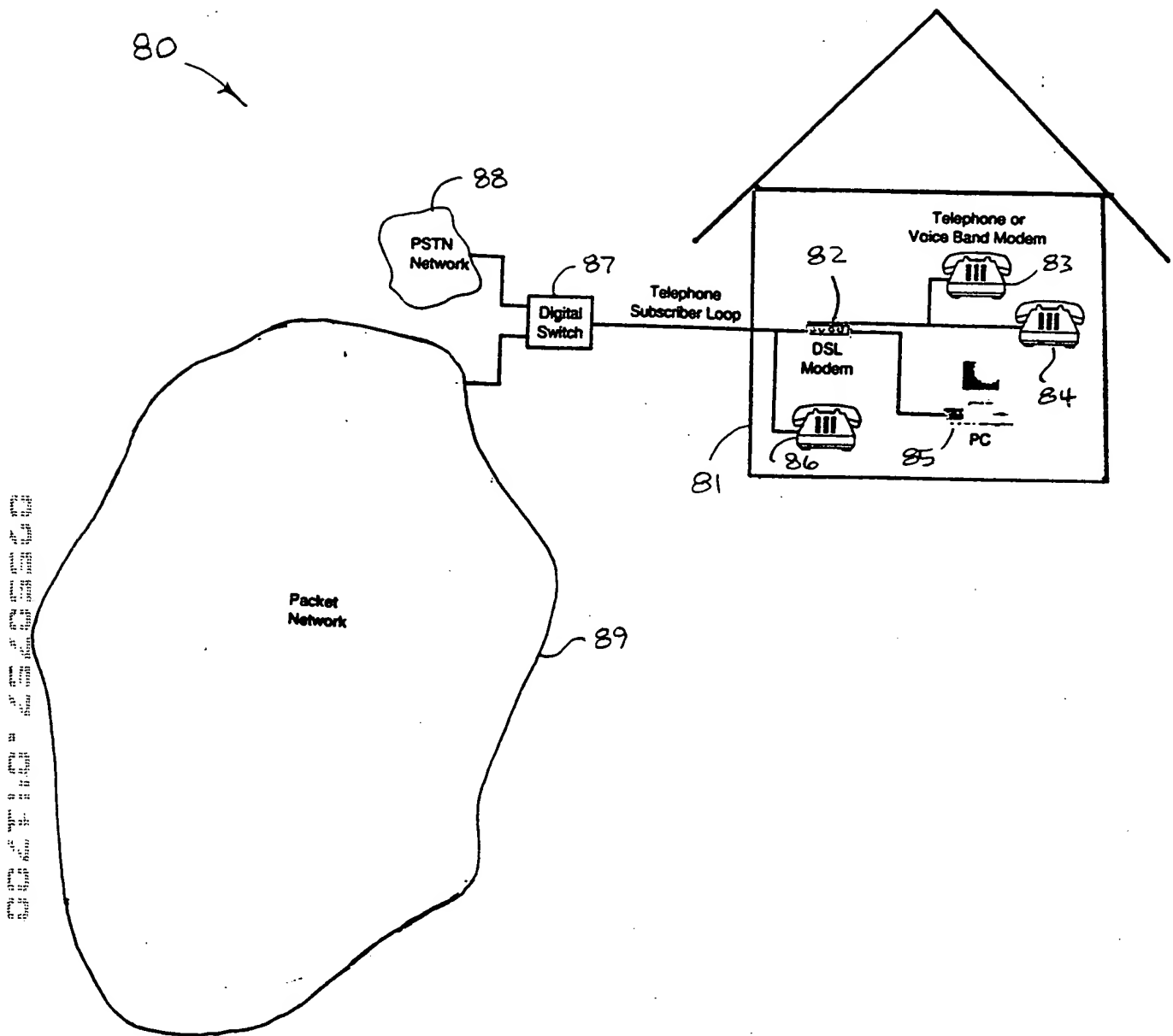
FIG. 6

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**FIG. 7**



**FIG. 8**

FIG. 9 is a block diagram of a digital communication system, such as a digital telephone system, which includes a digital signal processor (DSP) and a POTS splitter. The DSP is configured to process digital signals and is connected to a POTS splitter, which is connected to a twisted pair telephone loop. The DSP includes a transmit path and a receive path, each with various processing blocks such as error control coding, bit mapping, pre-coding, digital filtering, DACs, and analog filters. The receive path also includes an auto-gain and an adaptive equalizer. The POTS splitter is connected to a POTS telephone set or voice band modem.

FIG. 9

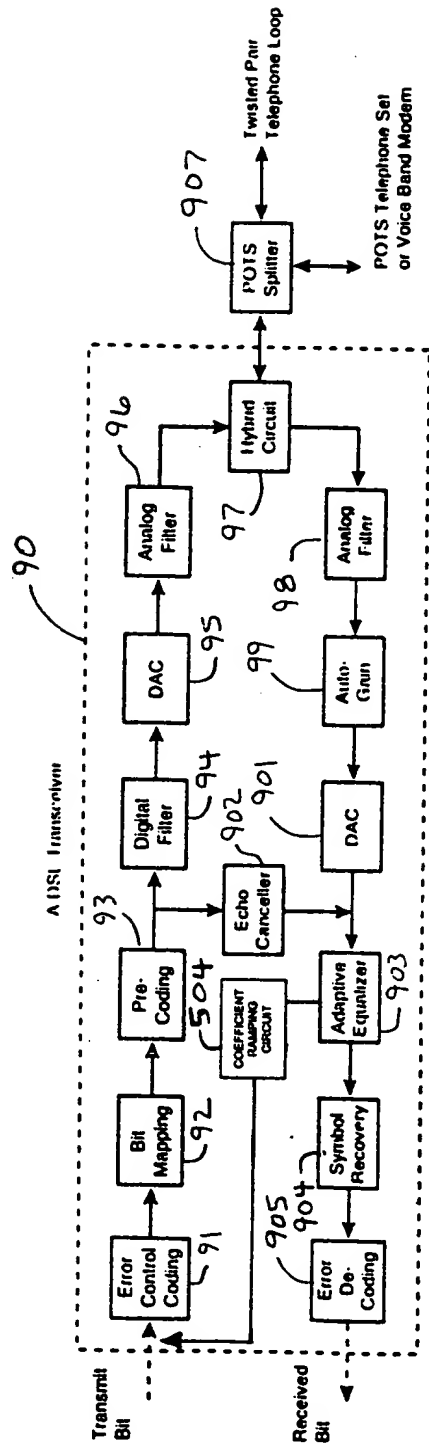




FIG. 10

